

PATENT

Docket No.: M4065.0335/P335-A

#12 Ansel B
Farensky
6/20/00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Howard Rhodes

Serial No.: 09/318,159

Filed: May 25, 1999

Group Art Unit: 2811

Examiner: G. Munson

For: TRENCH ISOLATION FOR
SEMICONDUCTOR DEVICES

Assistant Commissioner for Patents
Washington, D.C. 20231

AMENDMENT

RECEIVED
JUN 15 2000
TECHNOLOGY CENTER 2800

Dear Sir:

In response to the Office Action dated February 14, 2000 (Paper No. 6), please amend the above-identified U.S. patent application as follows:

IN THE CLAIMS:

Please cancel claims 43, 44, 47, 48, 53 and 58.

Please add the following new claims:

-- 68. An integrated circuit comprising:

a semiconductor substrate including a plurality of active regions;